



AO4438

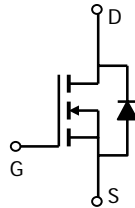
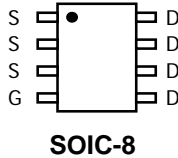
N-Channel Enhancement Mode Field Effect Transistor

General Description

The AO4438 uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications. *Standard Product AO4438 is Pb-free (meets ROHS & Sony 259 specifications). AO4438L is a Green Product ordering option. AO4438 and AO4438L are electrically identical.*

Features

- V_{DS} (V) = 60V
- I_D = 8.2A (V_{GS} = 10V)
- $R_{DS(ON)}$ < 22m Ω (V_{GS} = 10V)
- $R_{DS(ON)}$ < 27m Ω (V_{GS} = 4.5V)



Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^A	$T_A=25^\circ\text{C}$	8.2	A
		$T_A=70^\circ\text{C}$	
Pulsed Drain Current ^B	I_{DM}	40	
Power Dissipation	$T_A=25^\circ\text{C}$	3.1	W
		$T_A=70^\circ\text{C}$	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	24	40	$^\circ\text{C/W}$
Maximum Junction-to-Ambient ^A		Steady-State	54	
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	21	30	$^\circ\text{C/W}$

N Channel Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$	60			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=48\text{V}$, $V_{GS}=0\text{V}$ $T_J=55^\circ\text{C}$			1	μA
					5	
I_{GSS}	Gate-Body leakage current	$V_{DS}=0\text{V}$, $V_{GS}=\pm 20\text{V}$			100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	1	2.2	3	V
$I_{D(ON)}$	On state drain current	$V_{GS}=10\text{V}$, $V_{DS}=5\text{V}$	40			A
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$, $I_D=8.2\text{A}$ $T_J=125^\circ\text{C}$		16.3	22	m Ω
				30	40	
		$V_{GS}=4.5\text{V}$, $I_D=7.6\text{A}$		20	27	m Ω
g_{FS}	Forward Transconductance	$V_{DS}=5\text{V}$, $I_D=8.2\text{A}$		24		S
V_{SD}	Diode Forward Voltage	$I_S=1\text{A}$, $V_{GS}=0\text{V}$		0.74	1	V
I_S	Maximum Body-Diode Continuous Current				3	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{GS}=0\text{V}$, $V_{DS}=30\text{V}$, $f=1\text{MHz}$		1920	2300	pF
C_{oss}	Output Capacitance			155		pF
C_{rss}	Reverse Transfer Capacitance			116		pF
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $f=1\text{MHz}$		0.65	0.8	Ω
SWITCHING PARAMETERS						
$Q_g(10\text{V})$	Total Gate Charge	$V_{GS}=10\text{V}$, $V_{DS}=30\text{V}$, $I_D=8.2\text{A}$		47.6	58	nC
$Q_g(4.5\text{V})$	Total Gate Charge			24.2	30	nC
Q_{gs}	Gate Source Charge			6		nC
Q_{gd}	Gate Drain Charge			14.4		nC
$t_{D(on)}$	Turn-On DelayTime	$V_{GS}=10\text{V}$, $V_{DS}=30\text{V}$, $R_L=3.6\Omega$, $R_{GEN}=3\Omega$		8.2		ns
t_r	Turn-On Rise Time			5.5		ns
$t_{D(off)}$	Turn-Off DelayTime			29.7		ns
t_f	Turn-Off Fall Time			5.2		ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=8.2\text{A}$, $di/dt=100\text{A}/\mu\text{s}$		34	41	ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=8.2\text{A}$, $di/dt=100\text{A}/\mu\text{s}$		53		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design. The current rating is based on the $t \leq 10\text{s}$ thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

D. The static characteristics in Figures 1 to 6 are obtained using 80 μs pulses, duty cycle 0.5% max.

E. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The SOA curve provides a single pulse rating.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: N-CANNEL

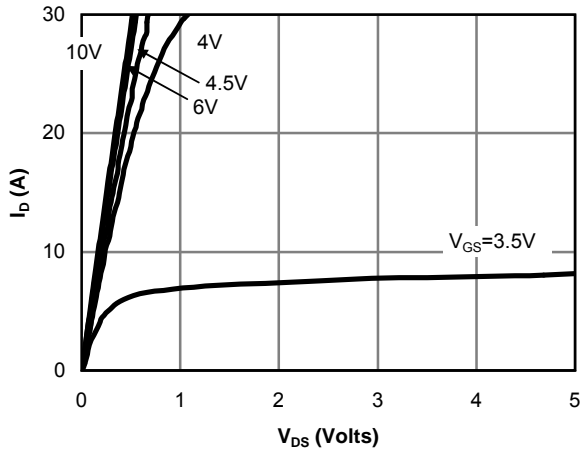


Fig 1: On-Region Characteristics

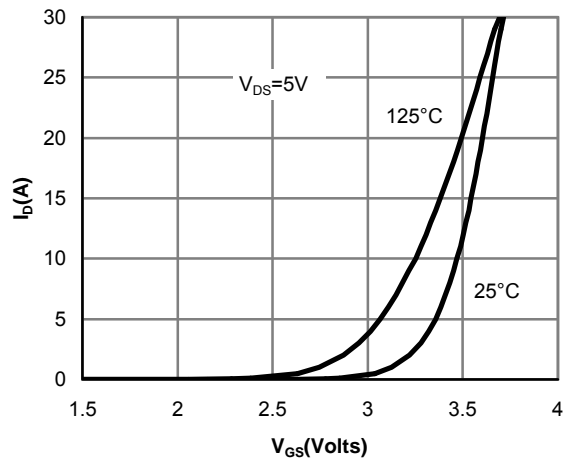


Figure 2: Transfer Characteristics

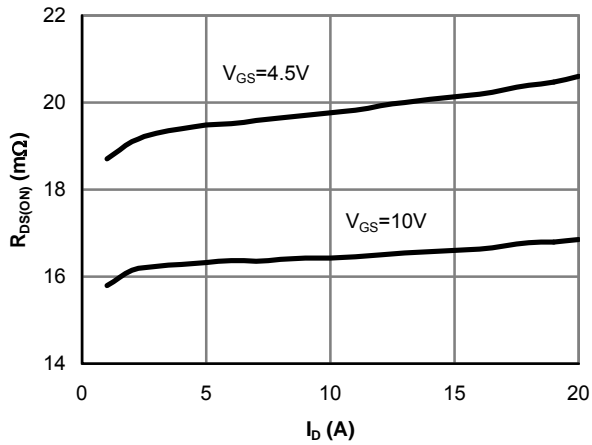


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

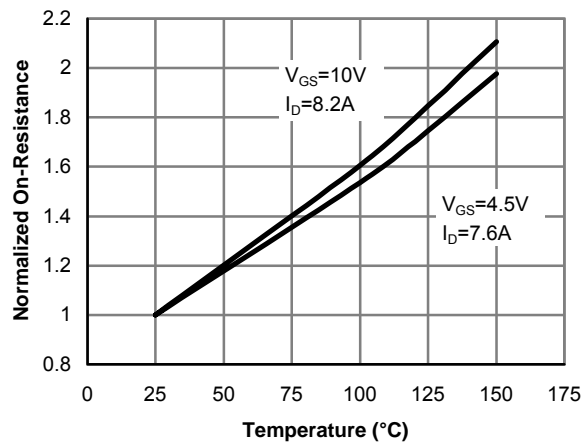


Figure 4: On-Resistance vs. Junction Temperature

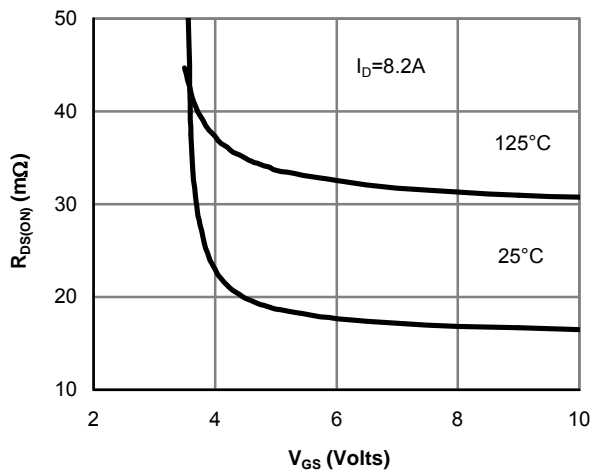


Figure 5: On-Resistance vs. Gate-Source Voltage

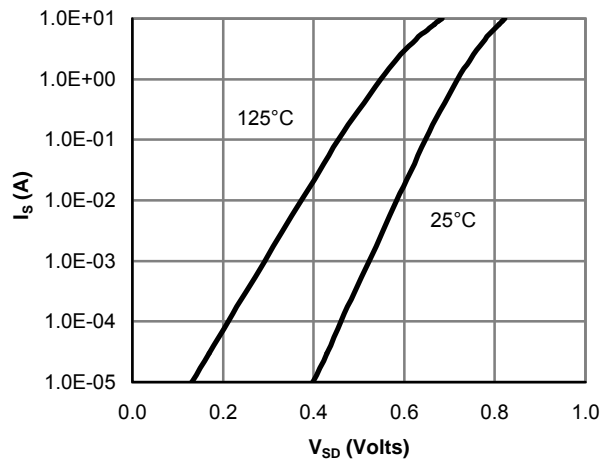


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: N-CANNEL

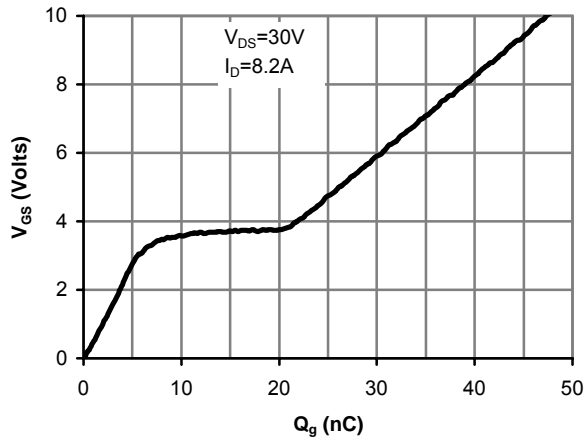


Figure 7: Gate-Charge Characteristics

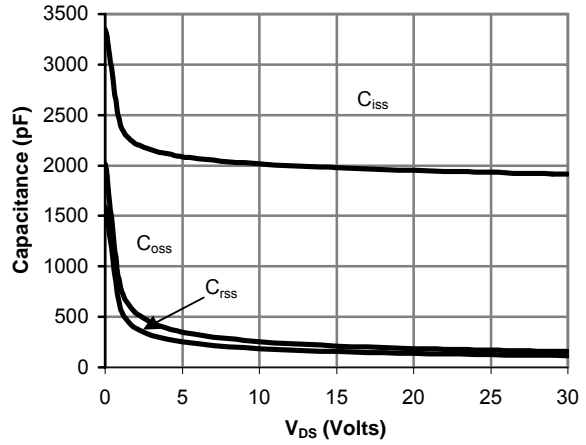


Figure 8: Capacitance Characteristics

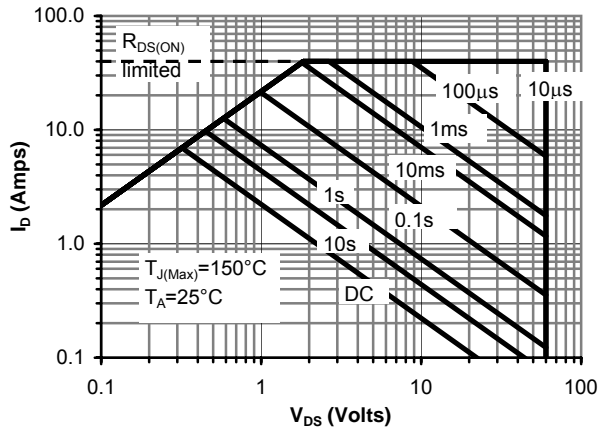


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

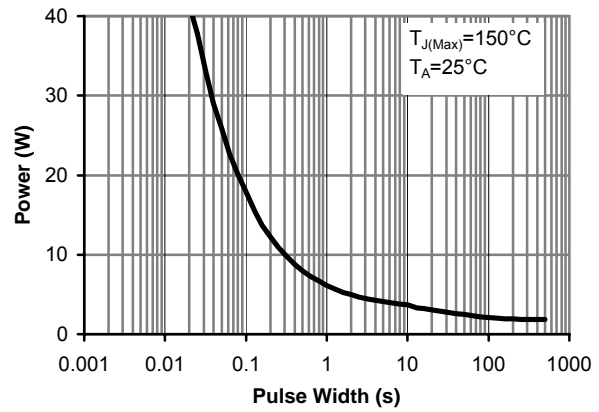


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

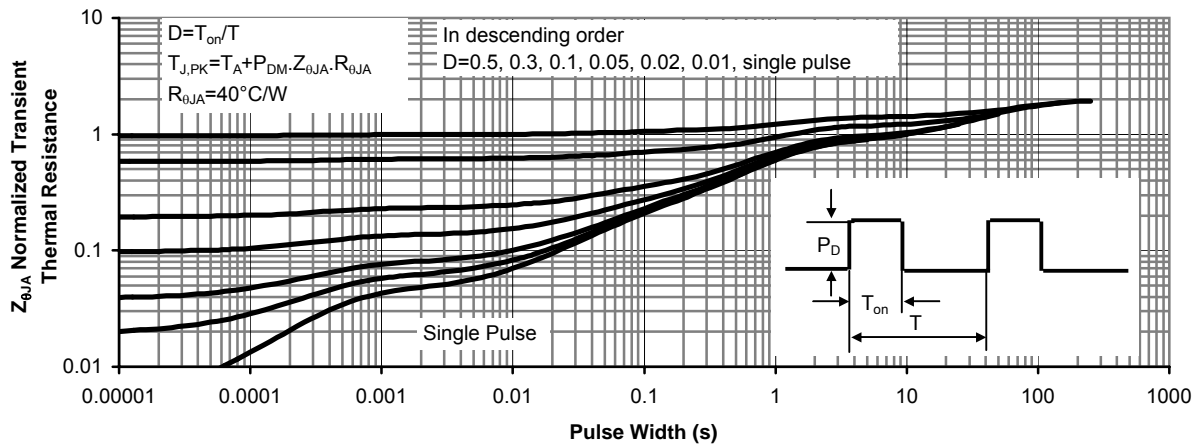


Figure 11: Normalized Maximum Transient Thermal Impedance